

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	284718	(cyclic or circular or ring or cycle) near3 (unit or device or apparatus or circuit or engine or module)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/07/07 14:19
L2	43544	alu or (arithmetic logic unit)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:21
L3	189092	("xor" or bitwise or exclusive)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:21
L4	5610	(barrel or circluar or circle) near3 shift\$3	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:22
L5	8	(l1 or l2) with l3 with l4	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:31
L6	13	(l1 or l2) same l3 same l4 same (buffer or accumul\$3 or register)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:33
L7	634	(l1 or l2) and l3 and l4 and (buffer or accumul\$3 or register)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:32
L8	0	l7 and (ldpc or (low density parity check))	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:38
L9	18	l3 with l4 with (buffer or accumul\$3 or register)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:34
L10	69	l3 same l4 same (buffer or accumul\$3 or register)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:35
L11	0	(ldpc or (low density parity check))with (matrix near2 (memory or ram or rom)) with register	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:39

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L12	0	(ldpc or (low density parity check)) with (matrix near2 (memory or ram or rom)) with register	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:48
L13	0	(ldpc or (low density parity check)) same (matrix near2 (memory or ram or rom)) same register	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:40
L14	0	(ldpc or (low density parity check)) same (matrix near2 (memory or ram or rom)) same (register or latch or "flip flop")	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:40
L15	1	(ldpc or (low density parity check)) same (matrix with (memory or ram or rom)) same (register or latch or "flip flop")	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:41
L16	4	(ldpc or (low density parity check)) same (memory or ram or rom) same (register or latch or "flip flop")	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:42
L17	2	(ldpc or (low density parity check)) same (I1 or I2)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:43
L18	22	(ldpc or (low density parity check)) and (I1 or I2)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:46
L19	164	(ldpc or (low density parity check)) near3 encod\$3	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:57
L20	4	I19 and (I1 or I2)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:46
L21	62	(memory or ram or rom) with (shift near4 weight)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:49
L22	106	(memory or ram or rom) with (shift near8 weight)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 15:04
L23	0	(ldpc or (low density parity check)) with (I21 or I22)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 15:04

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L24	0	(ldpc or (low density parity check)) same (I21 or I22)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:57
L25	0	(ldpc or (low density parity check)) and (I21 or I22)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 14:57
L26	11360	(memory or ram or rom) near8 weight	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 16:03
L27	0	(ldpc or (low density parity check)) with I26	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 15:05
L28	0	(ldpc or (low density parity check)) same I26	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 16:09
L29	6	(ldpc or (low density parity check)) and I26	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 16:02
L30	19613	(memory or ram or rom) with weight	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 16:08
L31	1887	((precalculat\$3) or (precomput\$3)) near8 (stor\$3 or memory or ram or rom)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 16:09
L32	85704	((precalculat\$3) or (precomput\$3) or weight or parameter) near8 (stor\$3 or memory or ram or rom)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 16:09
L33	0	(ldpc or (low density parity check)) same (I30 or I31 or I32)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 16:10
L34	13	(ldpc or (low density parity check)) and (I30 or I31 or I32)	USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2006/07/07 16:10